

PATENT APPLICATION

Sheet 2 of 2

FORM PTO-1449

ATTY. DOCKET NO.

10004753-1

APPLICATION NO.

09/853,738

CONFIRMATION NO.

APPLICANT

Duan Wiens et al.

FILING DATE

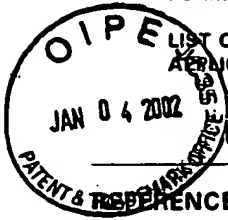
May 10, 2001

GROUP

2123

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)



REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
	2A				
	2B				
	2C				
	2D				
	2E				
	2F				
	2G				
	2H				
	2I				
	2J				
	2K				

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GROUP 2123

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	TRANSLATION	
						YES	NO
	2L						
	2M						
	2N						
	2O						
	2P						

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

J.A.	2Q	M. Papamarcos and J. Patel, "A Low Overhead Coherent Solution for Microprocessors with Private Cache Memories," in Proceedings of the 11th International Symposium on Computer Architecture, IEEE, New York (1984), pp. 348-354
	2R	
	2S	

EXAMINER

DATE CONSIDERED

11/14/04

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Sheet 1 of 2

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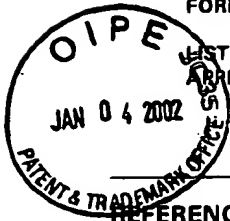
May 10, 2001

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STATEMENT

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REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
	1A					
	1B					
	1C					
	1D					
	1E					
	1F					
	1G					
	1H					
	1I					
	1J					
	1K					

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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	TRANSLATION	
							YES	NO
	1L							
	1M							
	1N							
	1O							
	1P							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

N.M.	1Q	De Blasi, "Computer Architecture," ISBN 0-201-41603-4 (Addison-Wesley, 1990), pp.273-291
N.M.	1R	Stone, "High Performance Computer Architecture," ISBN 0-201-51377-3 (Addison-Wesley, 2d Ed. 1990), pp.29-39
N.M.	1S	Tabak, "Advanced Microprocessors, " ISBN 0-07-062807-6 (McGraw-Hill, 1991) pp. 244-248

EXAMINER

DATE CONSIDERED

11/14/04